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A Block-Asynchronous Relaxation Method for Graphics ...

chronous relaxation methods on Graphics Processing Units (GPUs) For this purpose, we developed a set of asynchronous iteration algorithms in CUDA and compared them with a parallel implementation of synchronous relaxation methods on CPU-based systems For a set of test matrices taken from the University of Florida Matrix Collection we monitor the

George Karypis Computer Science and Engineering

Paper presented at 28th IEEE International Parallel and Distributed Processing Symposium (IPDPS 2014) LaSalle, D W, and George Karypis 2013 Nerstrand: Fast multi-threaded graph In Euro-par 2012 parallel processing, lecture notes in computer science, eds C Kaklamanis,

Hasan Metin Aktulga

ceedings of 2012 41st International Conference on Parallel Processing Workshops (ICPPW 2012), pp 71–80, Pittsburgh, PA, USA (Sep 2012) [ICPPW12] 7H M Aktulga, C Yang, E Ng, P Maris, and J Vary, “Topology-aware mappings for large-scale eigenvalue problems,” in Proceedings of Euro-Par 2012 Parallel Processing, Lecture

LNCS 7156 - Workshop on Resiliency in High Performance ...

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A Reliability Model for Cloud Computing for High ...

I Caragiannis et al (Eds): Euro-Par 2012 Workshops, LNCS 7640, pp 474–483, 2013 used in most parallel scientific applications resource allocation method for data processing

SoaAlloc: Accelerating Single-Method Multiple-Objects ...

Optimizations for GPUs In Euro-Par 2015: Parallel Processing Springer Berlin Heidelberg, Berlin, Heidelberg, 263–274 [11] C Maureira-Fredes and P Amaro-Seoane 2018 GraviDy, a GPU modular, parallel direct-summation N-body integrator: dynamics with softening Monthly Notices of the Royal Astronomical Society 473, 3 (2018), 3113–3127

Quantification of Uncertainty for Estimation, Simulation ...

QUESO is a parallel object-oriented statistical library dedicated to the research of statistically robust, scalable, load balanced, and fault-tolerant mathematical algorithms for the quantification of uncertainty (UQ) of mathematical models and their predictions The purpose of this chapter is to introduce relevant terminology, mathematical and

Achieving Data-Aware Load Balancing through Distributed ...

Coordinated Checkpoint/Rollback Protocol”, Euro-Par 2011: Parallel Processing Workshops, Volume 7156, 2012, pp 322-332 [4] I Raicu “Many-Task Computing: Bridging the Gap between High Throughput Computing and High Performance Computing”, ISBN: 978 ...

John Douglas Owens

Distinguished Paper, European Conference on Parallel and Distributed Computing (Euro-Par) 2018 (C Yang, A Buluç, and J D Owens, Design Principles for Sparse Matrix Multiplication on the GPU) Best Artifact Award, European Conference on Parallel and Distributed Computing (Euro-Par)

Curriculum Vitae Dick H.J. Epema

Sep 07, 2018 · · IEEE P2P Computing 2007, 2008, 2009, 2012 · Euro-Par 2007, 2008 (Global Chair topic Peer-to-Peer Systems) · Int’l Workshop on Peer-To-Peer Systems (IPTPS) 2008 · Int’l Parallel and Distributed Processing Symposium (IPDPS) 2008 · Workshop on Job Scheduling Strategies for

Parallel Processing (JSSPP), 2010, 2012-2017

CURRICULUM VITAE - CAPSL

-International Conference on Parallel Processing (EURO-PAR'95, 96, 01) - Compilers, Architectures and Synthesis for Embedded Systems (CASES'00, 01) - IEEE International Symposium on High Performance Computer (HPCA'97, 99, 00)

rCUDA 4: GPGPU as a service in HPC clusters

of GPU virtualization in high performance clusters”, in Euro-Par 2009, Parallel Processing - Workshops , 6043, pp 385-394, Lecture Notes in Computer Science, Springer-Verlag, 2010 Rafael Mayo

Reliable Performance Auto-tuning in presence of DVFS

Santos Costa, editors, Euro-Par 2014 Parallel Processing, volume 8632 of Lecture Notes in Computer Science, pages 63-74 Springer International Publishing, 2014 3 R C Whaley and J Dongarra Automatically Tuned Linear Algebra Software In Ninth SIAM Conference on Parallel Processing for Scientific Computing, 1999 CD-ROM Proceedings 4 R C

Nodari Sitchinava: Curriculum Vitae

Computational models for multicores and GPUs, parallel external memory and cache-oblivious algorithms, parallel data structures, energy-efficient computation, distributed processing of massive data Teaching Experience 1 Lectures and Seminars Lecturer, University of Hawaii, Manoa Spring 2014 Undergraduate course “ICS 491: Parallel Algorithms”

Nodari Sitchinava: Curriculum Vitae

In Proceedings of the 19th International Conference Euro-Par 2013 Parallel Processing (Euro-Par), pages 659-670, 2013 [C-14] L Arge, J Fischer, P Sanders, N Sitchinava On (dynamic) range minimum queries in external memory In Proceedings of the 13th International Symposium on Algorithms and Data Structures (WADS), pages 37-48, 2013